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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/651,924	08/31/2000	Michael S Bertone	1662-31400 (P00-3212)	4257

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EXAMINER

NGUYEN, DUSTIN

ART UNIT	PAPER NUMBER
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2154

DATE MAILED: 04/15/2004

7

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/651,924

Applicant(s)

BERTONE ET AL.

Examiner

Dustin Nguyen

Art Unit

2154

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 February 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-24 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

1. Claims 1 – 24 are presented for examination.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

3. Claim 21 is rejected under 35 U.S.C. 102(e) as being anticipated by Nattkemper et al. [US Patent No 5,999,518].
4. As per claim 21, Nattkemper discloses the invention substantially as claimed including a system, comprising:

a plurality of resources [104a-n, Figure 1; and col 3, lines 62-col 4, lines 10]; and

Art Unit: 2154

a receiver adapted to receive requests from said sources [col 4, lines 65-col 5, lines 14], the receiver comprising a controller that permits said sources to provide requests to said receiver based on credits issued by said receiver to said sources [col 5, lines 15-32].

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 1-20 and 22-24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nattkemper et al. [US Patent No 5,999,518], in view of Shah et al. [US Patent No 6,347,337].

7. As per claim 1, Nattkemper discloses the invention substantially as claimed including a multi-processor computer system, comprising:

a plurality of processors [401 a-n, Figure 1], and one interprocessor router [22, Figure 3; and col 6, lines 8-35]; and

a memory coupled to each processor, each memory managed by a memory controller configured to accept memory requests from the plurality of processors [78, 86, 97, Figure 4]; and

wherein the memory requests from a local processor are delivered to the memory controller by the cache control unit [col 6, lines 8-27] and wherein memory requests from other processors are delivered to the memory controller by the interprocessor router [col 6, lines 27-35

Art Unit: 2154

] and wherein the memory controller allocates the memory requests from the plurality of processors in a shared buffer using a credit-based allocation scheme [col 4, lines 62-col 5, lines 32].

Nattkemper does not disclose

each processor coupled to at least one memory cache, one cache control unit.

Shah discloses

each processor coupled to at least one memory cache, one cache control unit [75, Figure 7; and col 8, lines 42-51];

It would have been to a person skill in the art at the time the invention was made to combine the teaching of Nattkemper and Shah because Shah's teaching of cache would allow to increase the performance of the system.

8. As per claim 2, Nattkemper discloses wherein:

the interprocessor router are each assigned a number of credits [col 9, lines 37-51];

at least one of said credits must be delivered by the interprocessor router to the memory controller when a memory request is delivered by the interprocessor router to the memory controller [col 5, lines 19-22].

Nattkemper does not specifically disclose

the cache control unit are each assigned a number of credits;

at least one of said credits must be delivered by the cache control unit to the memory controller when a memory request is delivered by the cache control unit to the memory controller; and

Art Unit: 2154

wherein if the number of filled spaces in the shared buffer is below a threshold, the buffer return the credits to the source from which the credit and memory request arrived.

Shah discloses

the cache control units are each assigned a number of credits [col 8, lines 20-26];

at least one of said credits must be delivered by the cache control unit to the memory controller when a memory request is delivered by the cache control unit to the memory controller [col 10, lines 6-16]; and

wherein if the number of filled spaces in the shared buffer is below a threshold, the buffer return the credits to the source from which the credit and memory request arrived [col 13, lines 1-5].

It would have been obvious to a person skill in the art at the time the invention was made to combine the teaching of Nattkemper and Shah because Shah's teaching would allow to manage buffer and control the flow to prevent congestion.

9. As per claim 3, Shah discloses wherein:

wherein if the number of filled spaces in the shared buffer is above a threshold, the buffer holds the credits and returns a credit in a round-robin manner to a source from which a credit has been received only when a space in the shared buffer becomes free [col 13, lines 5-10]; and

wherein if a source has no available credits, that source cannot deliver a memory request to the shared buffer [106, Figure 9].

10. As per claim 4, Nattkemper discloses wherein:

Art Unit: 2154

the number of credits assigned to the cache control unit and the interprocessor router is sufficient to enable each source to deliver an uninterrupted burst of memory requests to the buffer without having to wait for credits to return from the buffer [col 13, lines 6-18].

11. As per claim 5, Nattkemper discloses wherein:

the number of credits available in the cache control unit and the interprocessor router are stored and updated in counters located in the cache control unit and the interprocessor router [col 11, lines 37-49].

Nattkemper does not specifically disclose

the number of credits spent by the cache control unit and the interprocessor router are stored and updated in counters located in the shared buffer.

Shah discloses

the number of credits spent by the cache control unit and the interprocessor router are stored and updated in counters located in the shared buffer [72, Figure 7].

It would have been obvious to a person skill in the art at the time the invention was made to combine the teaching of Nattkemper and Shah because Shah's teaching would allow to control the flows in a more efficient manner.

12. As per claim 6, Nattkemper discloses wherein:

the threshold is the point when the number of free spaces available in the buffer is equal to the total number of credits assigned to the cache control unit and the interprocessor router [col 13, lines 2-5].

13. As per claim 7, it is rejected for similar reasons as stated above in claims 1 and 2.

Furthermore, Nattkemper discloses an interprocessor and I/O router unit configured to send request and response commands from remote processors to the memory controller [col 8, lines 15-17]. Nattkemper does not specifically disclose a front-end directory in-flight table [col 15, lines 58-64] and L2 data cache and L2 instruction and data cache control unit configured to send request and response commands from the processor to the memory controller. Shah discloses a front-end directory in-flight table [col 15, lines 58-64] and L2 data cache and L2 instruction and data cache control unit configured to send request and response commands from the processor to the memory controller [col 9, lines 8-15]. It would have been obvious to a person skill the art at the time the invention was made to combine the teaching of Nattkemper and Shah because Shah's teaching of L2 cache would provide additional level of caching to increase system performance.

14. As per claim 8, it is rejected for similar reasons as stated above in claims 2 and 3.

15. As per claim 9, Nattkemper discloses wherein:

the credits are returned to the sources which have given up credits to the directory in-flight table in a random, equally probably manner [col 9, lines 37-51; and col 59, lines 7-13].

16. As per claim 10, it is rejected for similar reasons as stated above in claims 6 and 7.

Art Unit: 2154

17. As per claims 11 and 12, they are rejected for similar reasons as stated above in claims 5 and 7.

18. As per claim 13, it is rejected for similar reasons as stated above in claim 4. Furthermore, Nattkemper discloses wherein the number of credits available to the L2 instruction and data cache control unit and interprocessor and 110 router is determined by the round trip time required to send a credit to and receive a credit from the directory in-flight table [col 24, lines 7-15].

19. As per claim 14, it is rejected for similar reasons as stated above in claims 1-3.

20. As per claim 15, it is rejected for similar reasons as stated above in claim 9.

21. As per claim 16, Nattkemper discloses wherein:

when the number of empty buffer spaces is smaller than the buffer threshold and a buffer space becomes empty, returning a credit in a random, statistically skewed manner to one of the sources which have spent credits held by the buffer [col 22, lines 27-34].

22. As per claim 17, Nattkemper discloses

assigning a minimum number of credits to each source that is sufficient to allow each source to send a continuous sequence of data packets without waiting for returned credits [col 18, lines 41-45].

23. As per claim 18, it is rejected for similar reasons as stated above in claim 3.

24. As per claim 19, it is rejected for similar reasons as stated above in claim 6.

25. As per claim 20, it is rejected for similar reasons as stated above in claim 5.

26. As per claim 22, Nattkemper does not specifically disclose each credit corresponds to a single request. Shah discloses each credit corresponds to a single request [col 8, lines 20-26]. It would have been obvious to a person skill in the art at the time the invention was made to combine the teaching of Nattkemper and Shah because Shah's teaching would prevent congestion in the network.

27. As per claim 23, Nattkemper discloses a buffer adapted to receive a plurality of requests from said sources, and said credits are issued to said sources to permit said sources to provide said requests to said buffer [84, Figure 4].

28. As per claim 24, Nattkemper discloses the receiver issues credits among said sources to avoid a source from having exclusive access to said receiver to the exclusion of the other sources [col 37, lines 20-30].

Art Unit: 2154

29. Applicant's arguments with respect to claims 1-24 have been considered but are moot in view of the new ground(s) of rejection.

30. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dustin Nguyen whose telephone number is (703) 305-5321. The examiner can normally be reached on Monday – Friday (8:00 – 5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Follansbee can be reached on (703) 306-8498.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directly to the receptionist whose telephone number is (703) 305-3900.

Dustin Nguyen



**JOHN FOLLANSBEE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100**